



## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Machington, D.C. 20201 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
08/993,442	12/18/1997	EARLE W. JENNINGS III	939A-040310	5108
75	90 06/24/2002			
ROBERT C COLWELL			EXAMINER	
TOWNSEND AND TOWNSEND AND CREW TWO EMBARCADERO CENTER 8TH FLOOR			NGO, CHUONG D	
SAN FRANCIS	CO, CA 94111		ART UNIT	PAPER NUMBER
			2124	

DATE MAILED: 06/24/2002

Please find below and/or attached an Office communication concerning this application or proceeding.



## Application No. Applicant(s) 88/993,442 Jennines I Office Action Summary Examiner Group Art Unit ---The MAILING DATE of this communication appears on the cover sheet beneath the correspondence address---**Period for Reply** A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE - HATEL MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, such period shall, by default, expire SIX (6) MONTHS from the mailing date of this communication . - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Status This action is FINAL. ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 1 1; 453 O.G. 213. **Disposition of Claims** S/Claim(s) 1,3 and 10-20 is/are pending in the application. is/are withdrawn from consideration. Of the above claim(s)\_\_\_\_ ☐ Claim(s)\_\_\_\_ \_\_\_\_\_ is/are allowed. © Claim(s) 1,3 and 10 - 20 is/are rejected. ☐ Claim(s)— ☐ Claim(s)— are subject to restriction or election requirement. **Application Papers** ☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948. ☐ The proposed drawing correction, filed on \_\_\_\_\_\_\_\_ is ☐ approved ☐ disapproved. ☐ The drawing(s) filed on\_\_\_\_\_\_ is/are objected to by the Examiner. $\hfill\Box$ The specification is objected to by the Examiner. ☐ The oath or declaration is objected to by the Examiner. Priority under 35 U.S.C. § 119 (a)-(d) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 11 9(a)-(d). ☐ All ☐ Some\* ☐ None of the CERTIFIED copies of the priority documents have been ☐ received in Application No. (Series Code/Serial Number)\_ ☐ received in this national stage application from the International Bureau (PCT Rule 1 7.2(a)). \*Certified copies not received: Attachment(s) ☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). ☐ Interview Summary, PTO-413 ☐ Notice of Reference(s) Cited, PTO-892 ☐ Notice of Informal Patent Application, PTO-152

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

Office Action Summary

□ Other \_\_\_

2

Serial No. 08/993,442

Ärt Unit: 2124

## **DETAIL OF ACTION**

1. Claims 1,3 and 10-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Deering et al. (5,745,125).

Deering et al. discloses in figures 2-13 an integrated circuit (112, figures 3 and 4) operating with a memory (106). The integrated circuit have an interfacing circuit and embedded processor (142, figures 5), and an array processor (152). The array processor have a plurality of floating circuits, each including a first MAC unit (514,516) with a first local memory (512, see figure 11), a second MAC (614,616) with a second local memory (612, see figures 13), and a share operand (SRAM 153) as claimed. It is noted that Deering et al. does not specifically discloses the integrated circuit for image frame rendering. However, since Deering et al. disclose the circuit for graphic accelerator, it would have been an obvious field of use to apply the teaching of Deering et al in image frame rendering as claimed. Further, the number of wires in the interface circuit (figure 5) would obviously exceed 256 wires, and the use of simplified IEEE floating point notation in the computation would have been an obvious matter of design choices in choosing the format of data.

2. Applicant's arguments filed on 04-22-2002 have been fully considered but they are not persuasive.

Applicant traverses the rejection by arguing that the shared operand unit (SRAM 153) of Deering et al. is not coupled to a second MAC unit (614,616) as required by the claim.

Serial No. 08/993,442

Art Unit: 2124

3

However, this feature is clearly disclosed by Deering et al. as the coupling between the shared operand unit (SRAM 153) and a second MAC unit (614,616) via FD-bus that receives data from SRAM and provides the data to FS Registers/buffer (384) to be accessed by a second MAC unit (614,616) (see figures 11 and 13).

3. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

4. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Serial No. 08/993,442

Art Unit: 2124

4

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chuong D. Ngo whose telephone number is (703) 305-9764. The examiner can normally be reached on Monday-Friday from 7:30 AM to 6:00 PM.

The fax phone number for the organization where this application or proceeding is assigned are as follows:

(703) 746-7238	(After Final Communication)
(703) 746-7239	(Official Communication)
(703) 746-7240	(For Status inquiries, draft communication)

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Chuong D. Ngo Primary Examiner

Art Unit 2124

06-21-02.